B2 cont

the source being coupled to the second power line.

B3

8. (Amended) The output buffer of claim 1, wherein the first end and the second end are respectively comprised of a fourth doped region of the second conductivity type and a fifth doped region of the second conductivity type.

B4

14. (Amended) The output buffer of claim 1, wherein the substrate is coupled to the second power line through a sixth doped region.

B5

34. (Amended) An electrostatic discharge protection circuit coupled between a first node and a second node, comprising:

a substrate of a first conductive type;

a first doped region and a second doped region of a second conductive type formed in the substrate, the first doped region and the second doped region being spaced apart enabling a channel region formed in between;

a well region of the second conductive type, formed in the substrate; and

a third doped region of the first conductive type, electrically floated in the well region, wherein the first node is electrically coupled to the first doped region and the second node is electrically coupled to the second doped region.

Please cancel claim 36 without prejudice.